

In a liquid crystal display comprising a panel with an antiferroelectric layer sandwiched between a pair of substrate electrodes and signal electrodes, a driving voltage waveform in which the peak value of the driving voltage waveform applied during a selection period is equal to the peak value of a scanning voltage waveform applied during a non-selection period is used. The structure controlling voltage waveform is determined by the length of time. The liquid crystal display includes a temperature sensor, and information output from the temperature sensor is used to modify the driving voltage waveform with a temperature compensation factor. The equalizing voltage is output as the layer structure controlling voltage waveform for the predetermined

In a liquid crystal display comprising a liquid crystal panel with an antiferroelectric liquid crystal sandwiched between a pair of substrates having scanning electrodes and signal electrodes, a display driving voltage waveform in which the peak value of a scanning voltage waveform applied during a selection period is set equal to the peak value of a scanning voltage waveform applied during a non-selection period is output as a layer structure controlling voltage waveform for a predetermined length of time. The liquid crystal panel includes a temperature sensor, and in accordance with the information output from the temperature sensor, the display driving voltage waveform with both peak values set equal is output as the layer structure controlling voltage waveform for the predetermined length of time.